Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	10	((finite adj state adj machine) (flip adj flop)) near3 detect near3 ((boundar$4 edge bord$5) word frame) near3 input	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/27 21:01
S2	8174	programmable adj (device circuit element)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/27 20:59
S7	2466	(state adj machine) near5 (boundar$4 edge bord$5 word frame)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/27 21:09
S8	69	S2 and S7	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/28 11:43
S9	98	S7 and SONET	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/27 21:09
S11	8174	programmable adj (device circuit element)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/28 14:13
S12	2466	(state adj machine) near5 (boundar$4 edge bord$5 word frame)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/28 14:11
S13	69	S11 and S12	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/28 13:43

S14	296	(state adj machine) near5 detect$5 near5 (boundar$4 edge bord$5 word frame)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/28 14:11
S15	17	S11 and S14	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/28 14:11
S16	30740	program$5 adj (device circuit element)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/28 14:41
S17	132	S12 and S16	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/28 14:13
S18	18	S14 and S16	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/28 14:15
S20	1	program$5 adj (device circuit element) same ((state adj machine) near5 detect$5 near5 (boundar$4 edge bord$5 word frame))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/28 15:34
S21	8	(FPGA pla pld) same ((state adj machine) near5 detect$5 near5 (boundar$4 edge bord$5 word frame))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/28 15:35
S22	104	(FPGA pla pld) with (detect$5 near5 (boundar$4 edge bord$5 word frame))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/28 15:36

S23	0	S22 and ((FPGA PLA PLD) with (FSM (finite adj state adj machince)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/28 15:37
S24	0	S22 and ((FPGA PLA PLD) and (FSM (finite adj state adj machince)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/28 15:37
S25	365	((FPGA PLA PLD) and (FSM (finite adj state adj machince)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/28 15:37
S26	0	S22 and S25	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/28 15:37
S27	114	S25 and (detect$5 near5 (boundar$4 edge bord$5 word frame))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/01 14:42
S32	100	fram$4 and word and align$6 and (program$5 adj circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/29 14:51
S33	68	(SONET SDH SPI4) with align	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/29 15:06
S34	65	S33 and frame	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/29 15:05

S35	39	(SONET SDH SPI4) with (align near8 frame)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/29 15:06
S36	366	((FPGA PLA PLD) and (FSM (finite adj state adj machince)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/01 14:43
S37	115	S36 and (detect$5 near5 (boundar$4 edge bord$5 word frame))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/01 14:43
S38	14	S37 and (SONET SDH SPI4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/02 16:23
S39	172	SPI4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/02 16:23
S40	13	OIF adj SPI4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/02 17:37